# ELECTROMETER-ULTRA LOW BIAS CURRENT MODELS 310, 311, 41, 42, AD523

#### GENERAL DESCRIPTION

Amplifiers with bias currents less than 1pA are classified as suitable for electrometer use where frequency response and voltage drift are usually secondary requirements. Both varactor bridge and FET input designs are employed to achieve these bias currents ranging from one pico amp  $(10^{-12} \, \text{A})$  to ten femptoamps  $(10^{-14} \, \text{A})$ .

Available with either inverting, noninverting or differential inputs, these amplifiers are used as current to voltage converters with high impedance transducers such as photomultiplier tubes, flame detectors, pH cells and radiation detectors. To minimize RFI and other noise pickup problems, the variactor modulated amplifiers, operating at 10fA, are available with shielded asset.

VAPACTOR BRIDGE ELECTROMETERS

MODEL 310 (INVERVING), MODEL 311 (NOVINVERTING), MODEL 311 (NOVIN

Typical specifications for models 310 and 311 include open loop gain of 100dB, 2kHz unity gain response, 0.4V/msec slew rate, initial bias current of  $10^{-14}$  A, with  $10^{-15}$  A/°C current stability, and low current and voltage noise of  $10^{-15}$  A and  $10\mu$ V p-p (1Hz bandwidth) respectively. Two voltage drift selections are available: 310J, 311J with  $30\mu$ V/°C, and 310K, 311K with  $10\mu$ V/°C. Each is housed in an aluminum enclosure for improved shielding.

Model 310, with inverting input only, is most appropriate for use with current source signals such as gas chromatographs flame detectors and photomultiplier tubes. It is also useful for precision long term integrators or where extremely wide dynamic current range is needed as in log compression amplifiers. Current to voltage converters may also be developed using a feedback resistor for setting the conversion scale factor.

Model 311 has a single noninverting input for measuring voltage from very high source impedances where bias currents would create substantial offset errors. Such sources include pH cells or stored capacitor charge as found in long term track and hold applications. Common mode rejection is  $100 {\rm dB}$  at  $\pm 25 {\rm V}$  with  $10^{14} \Omega$  impedance to ground for reduced source loading errors.

### FET INPUT ELECTROMETERS

MODELS 41, 42, AD523 (MONOLITHIC): This family of FET input amplifiers fully complements the varactor bridge



$I \sim 7 \sim$	Lowest Cost
	High Gain FET
	42
Model	/ J / K →
Open Loop Gain	
DC Rated Load min	3 <del>00,0</del> 00
Rated Output, min	±10V@5mA
Frequency Response	
Unity Gain, Small Signal	1MHz
Full Power Response, min	4kHz
Slewing Rate, min	0.25V/µs
Overload Recovery	10ms
Input Offset Voltage	
Initial, 25°C, (adj. to zero)	±2mV <sup>1</sup>
Avg. vs. Temp (0 to 70°C) max vs. Supply Voltage	±50   ±15   ±25μV/°C
vs. Supply voltage vs. Time	±25 µV/%
Input Bias Current	±250µV/mo.
Initial, 25°C, max	35054 1 20054 1 2554
Inverting Input (Varactor)	350fA   100fA   75fA
Non-Inverting Input (Varactor)	***
Avg. vs. Temp (0 to 70°C)	0, -4pA <sup>3</sup>
Input Impedance	7, 191
Differential	10 <sup>13</sup> Ω//3pF
Inverting Input (to common)	
Non-Inverting Input (to common)	
Common Mode (FET)	$10^{13} \Omega$
Input Noise	0310399
Voltage, 0.01 to 1Hz, p-p	6μV
5Hz to 50kHz, rms	8μV
Current, 0.1 to 10Hz, p-p	5fA
1 to 100Hz, rms (Varactor)	
Input Voltage Range Common Mode Voltage, min	±10V
Common Mode Rejection	66dB@±1V
Max Safe Differential Voltage	±15V
Power Supply Range (VDC)	±(12 to 18)V
Rated Specification (VDC)	±15V@2mA
Temperature Range	-137621111
Operating, Rated Specifications	0 to +70°C
Package Outline	QB-1
Case Dimensions	1.1" x 1.1" x 0.57"
Price	111 2 111 2 9.31
1-9	\$32   \$38   \$42
10-24	\$30   \$36   \$39
(1)With external 4.99k trim.	(3)Max bias at 70
(2)With trim terminal open.	(4)Signal input of

designs for electrometer applications. Available in three package sizes, these designs provide high input impedance, sub-picoamp bias currents and improved bandwidth characteristics. They may be used single-ended or differentially for making low level current or voltage measurements from photo/ion current transducers, pH cells, photometers or, in general, where speed and low input capacitance are essential for accurate measurements at high impedance levels as found in automated test systems. Other applications include fast integrators, charge amplifiers, differentiators and long term integrators. In addition, these carrier-less units overcome certain RFI problems which may arise in extremely noisy environments using the varactor bridge modulator types.

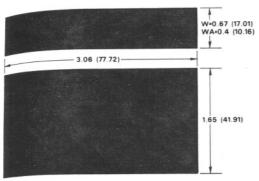
Model 42 J/K/L: Undoubtedly one of the best values for OEM designs, this differential FET amplifier has 110dB open loop gain, for improved closed loop accuracy, 1MHz unity gain response and CMR of 66dB at ±1V CMV. It is available in three current selections ranging from 0.35pA to 75fA. Each device features all hermetically sealed semiconductors, with monolituic front end in a compact module for improved reliability and good thermal transient response.

Model 41 J/K/L: This device combines outstanding bias current and drift specifications with speed and full differential input capability for use in a broad range of electrometer and integrator applications as well as for wideband differential and buffer circuitry. Typical specifications include 50kHz full power response, 94dB CMR at  $\pm 5V$  (80dB at  $\pm 10V$ ), 100dB gain for improved closed loop performance, and three bias current and drift selections: 41J, 0.5pA and  $25\mu V/^{\circ}$ C; 41K, 0.25pA and  $10\mu V/^{\circ}$ C; and 41L, 0.15pA and  $25\mu V/^{\circ}$ C. Special packaging techniques assure  $10^{13}$  input impedance, free from internal current leakage paths, and a maximum 4pA bias current rating at  $+70^{\circ}$ C.

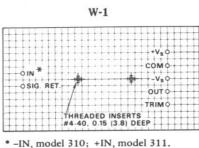
Model AD523: This unit is a very low bias current IC op amp. It features maximum steady-state bias currents (either input) as low as 0.25pA, in a special low-leakage TO-99 metal can package that minimizes case leakage by utilizing a special guard pin and high resistivity glass insulation. The AD523 is short circuit protected and offset voltage nullable, and features drift of  $15\mu\text{V}/^{\circ}\text{C}$ , slew rate of  $4\text{V}/\mu\text{sec}$ , and large signal voltage gain of 25,000 V/V. It is available in J, K, L (0 to +70 °C) and S (-55 °C to +125 °C) specification versions. (See also Linear IC Section).

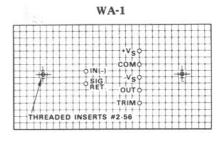
			$\sim$
Discrete			Microsit
Wideband High CMR 41	Varactor Inverting 310	Varactor Non-Leverting 311	Differential 0.25pA Guarded Input AD523
J K L	ј к	J	
100,000 ±10V@5mA	100,000 ±10V@5mA	100,000 ±10V@5mA	25,000 40,000 +0,000 ±10V@5mA
1MHz 50kHz 3V/µs 2µs	2kHz 7Hz 0.4V/ms 10ms	2kHz 7Hz 0.4V/ms 10ms	500kHz 50kHz typ 3.0V/μs 6μs
±2mV <sup>2</sup> ±25   ±10	Adjust to zero ±30 ±10μV/°C ±100μV/% ±100μV/mo.	Adjust to zero ±30  ±10μV/°C ±100μV/% ±100μV/mo.	±50mV ±20mV ±20mV ±90 ±30 ±60μV/°C ±30 ±15 ±15μV/%
0, -0.5pA   -0.25pA   -0.15pA 	±10fA ±1nA ±1fA/°C <sup>4</sup>	±1nA ±10fA - ±1fA/°C <sup>4</sup>	0, -1.0pA   -0.5   -0.25pA  2x/10°C
10 <sup>13</sup> Ω//3pF  10 <sup>13</sup> Ω	3 x 10 <sup>11</sup> Ω	$3 \times 10^{11} \Omega$ $10^{9} \Omega$ $10^{14} \Omega$	10 <sup>12</sup> Ω ————————————————————————————————————
8μV 10μV 5fA	10µV 10µV(1 to 100Hz) 1fA (0.01 to 1Hz) 2fA	10 \( \mu \text{V} \) 10 \( \mu \text{V} \) 10 \( \mu \text{V} \) 16 \( (0.01 \text{ to } 100 \text{Hz}) \) 26 \( \mu \text{2} \)	20μV  
±10V 94dB@±5V ±15V	NA NA ±300V	±25V 100dB@±25V ±300V	±8V 70dB min   80dB min   ±10V
±(12 to 18)V ±15V@8mA	±(12 to 18)V ±15V@+15, -6mA	±(12 to 18)V ±15V@+15, -6mA	±(5 to 18)V ±15V@7mA
0 to +70°C F-2 1.5" x 1.5" x 0.4"	0 to +70°C W-1 3" x 1.65" x 0.67"	0 to +70°C W-1 3" x 1.65" x 0.67"	0 to +70°C TO-99   (guard pin 8 conn. to case)
\$53 \$64 \$75 \$50 \$59 \$71	\$59 \$95 \$55 \$90	\$62 \$100 \$58 \$95	\$21 \$25 \$28 \$21 \$25 \$28

# W, WA PACKAGES

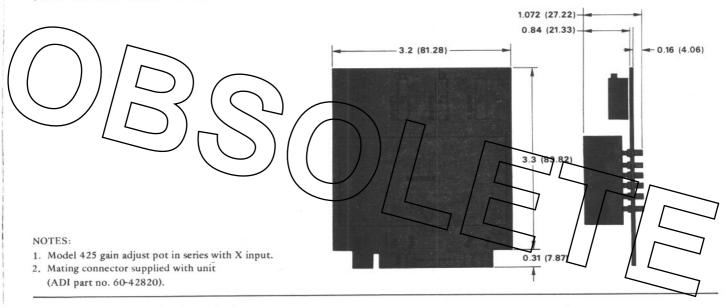


Model	Package	Socket
231	WA-1	AC1014
310	W-1	AC1017
311	W-1	AC1017

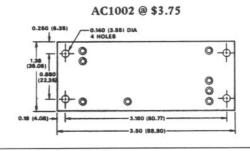




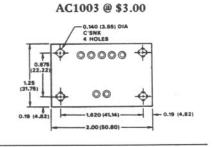
## MODEL 425 OUTLINE

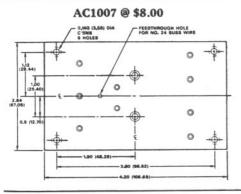


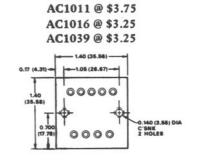
### MATING SOCKETS

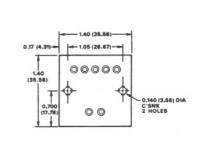


AC1008 @ \$3.25









AC1010 @ \$3.25